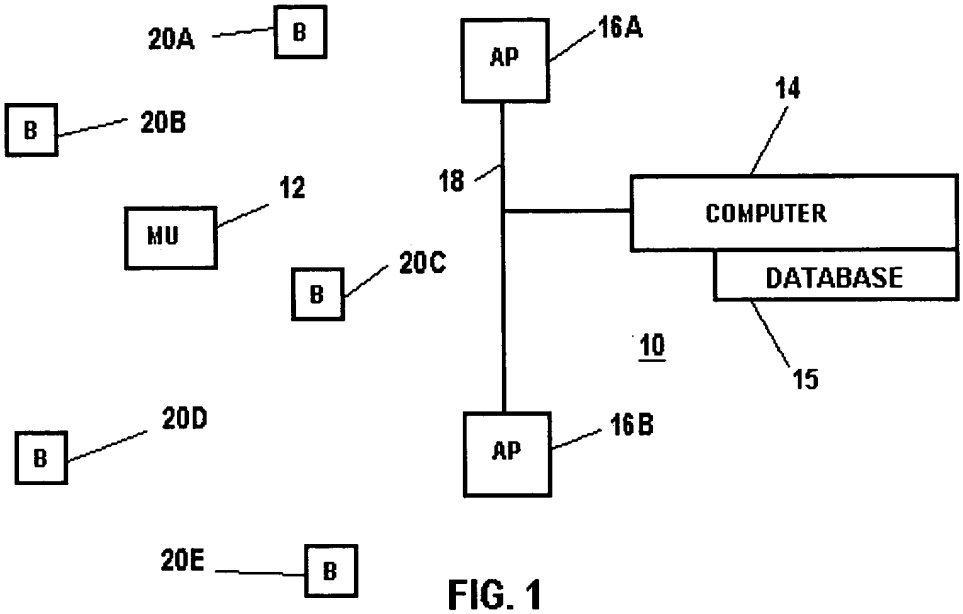
20A
B
AP
16A
B
20B
14
18
12
MU
COMPUTER
B
20C
DATABASE
10
15
B
20D
AP
16B
20E
B

FIG. 1

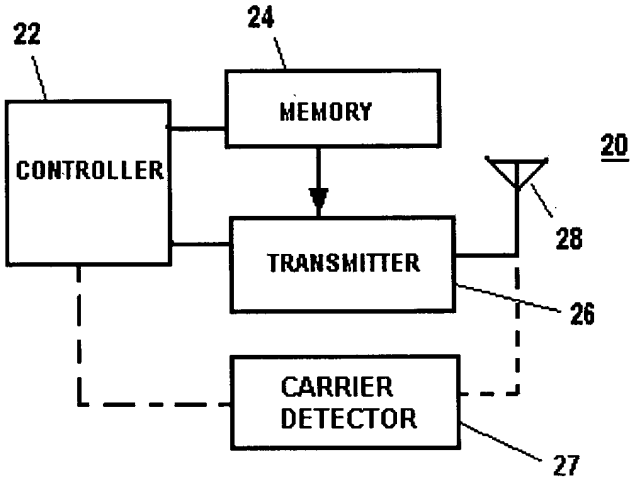
22
24
MEMORY
CONTROLLER
20
28
TRANSMITTER
26
CARRIER
DETECTOR
27

FIG. 2

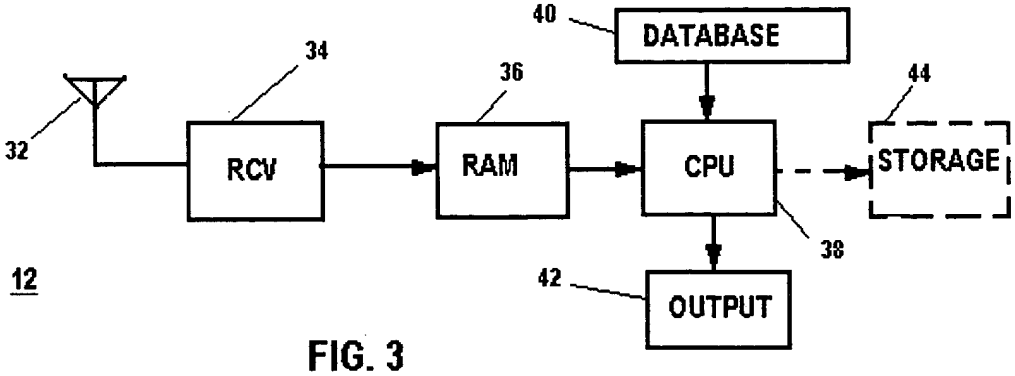
40
DATABASE
34
36
44
32
RCV
RAM
CPU
STORAGE
38
12
42
OUTPUT

FIG. 3

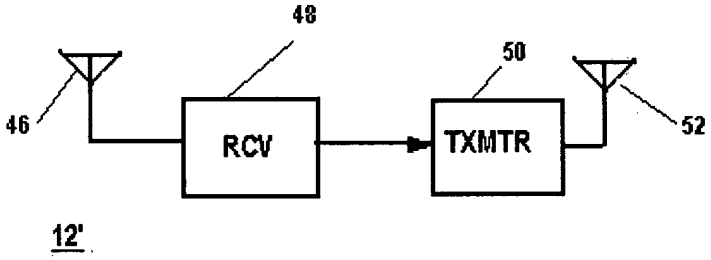
48
50
46
RCV
TXMTR
52
12'

FIG. 4